

FIG. 1

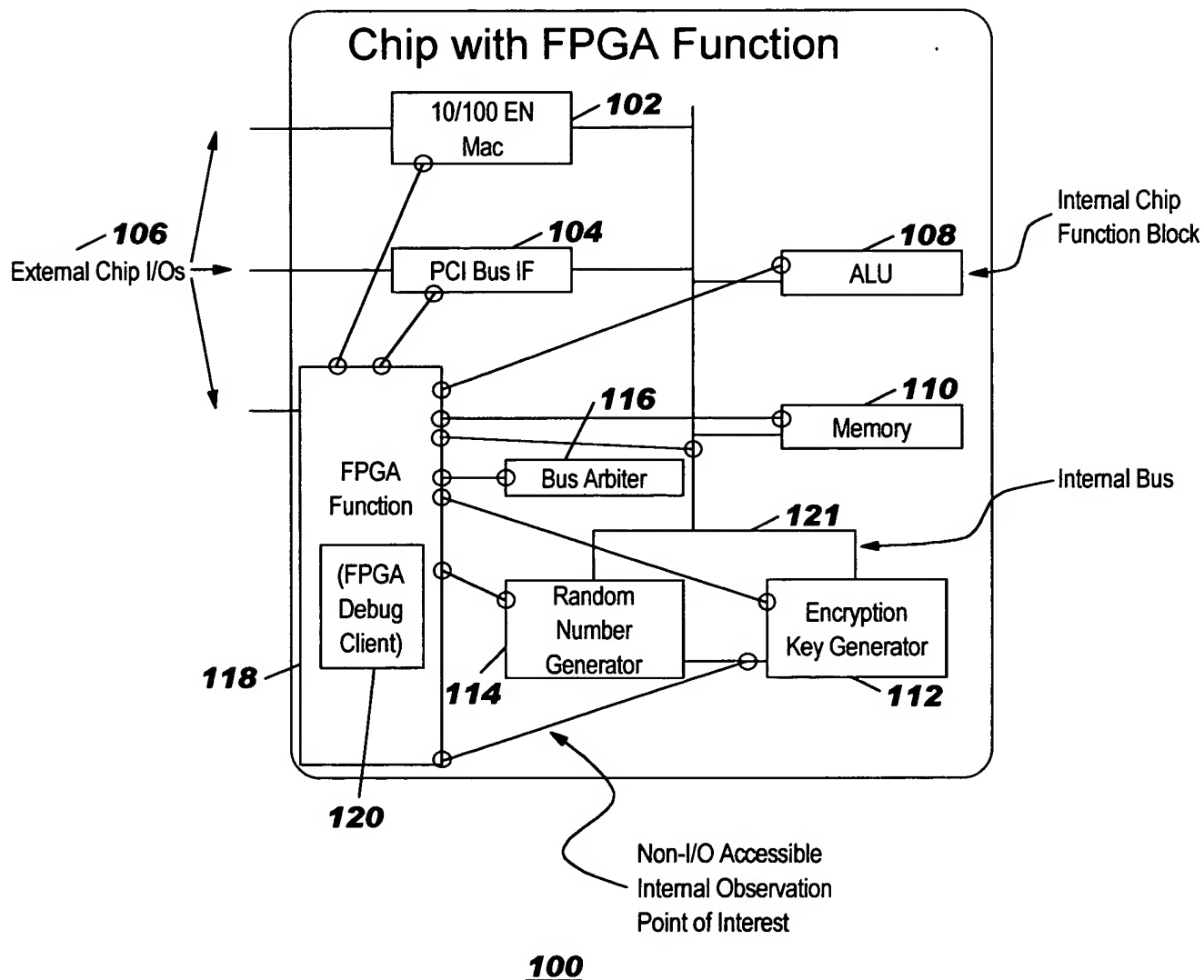


FIG. 2

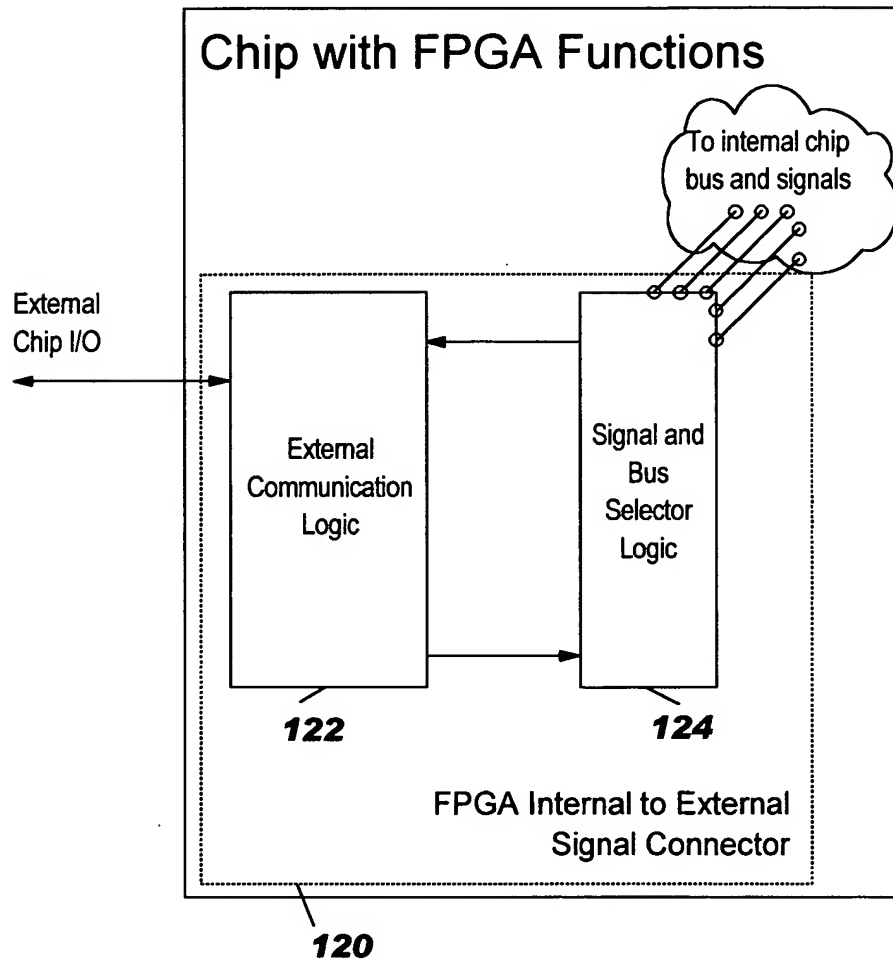


FIG. 3

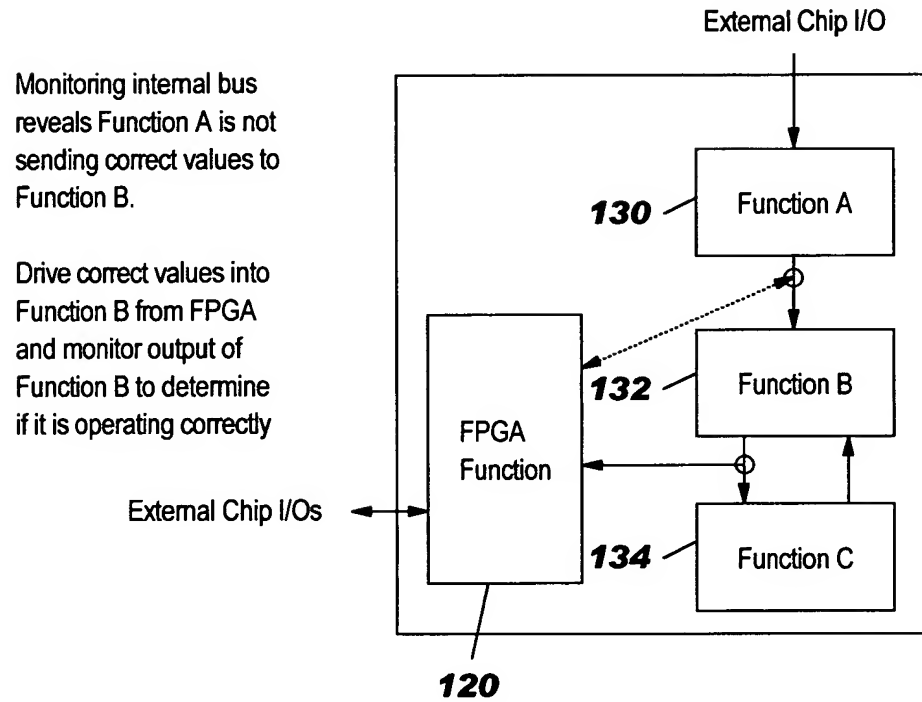


FIG. 4

- Memory and Memory Bus Debug
- MAC Bus Debug
- PCI, Internal Bus and Bus Arbiter Debug
- Processor Debug
- Clock and Clock Distribution Debug

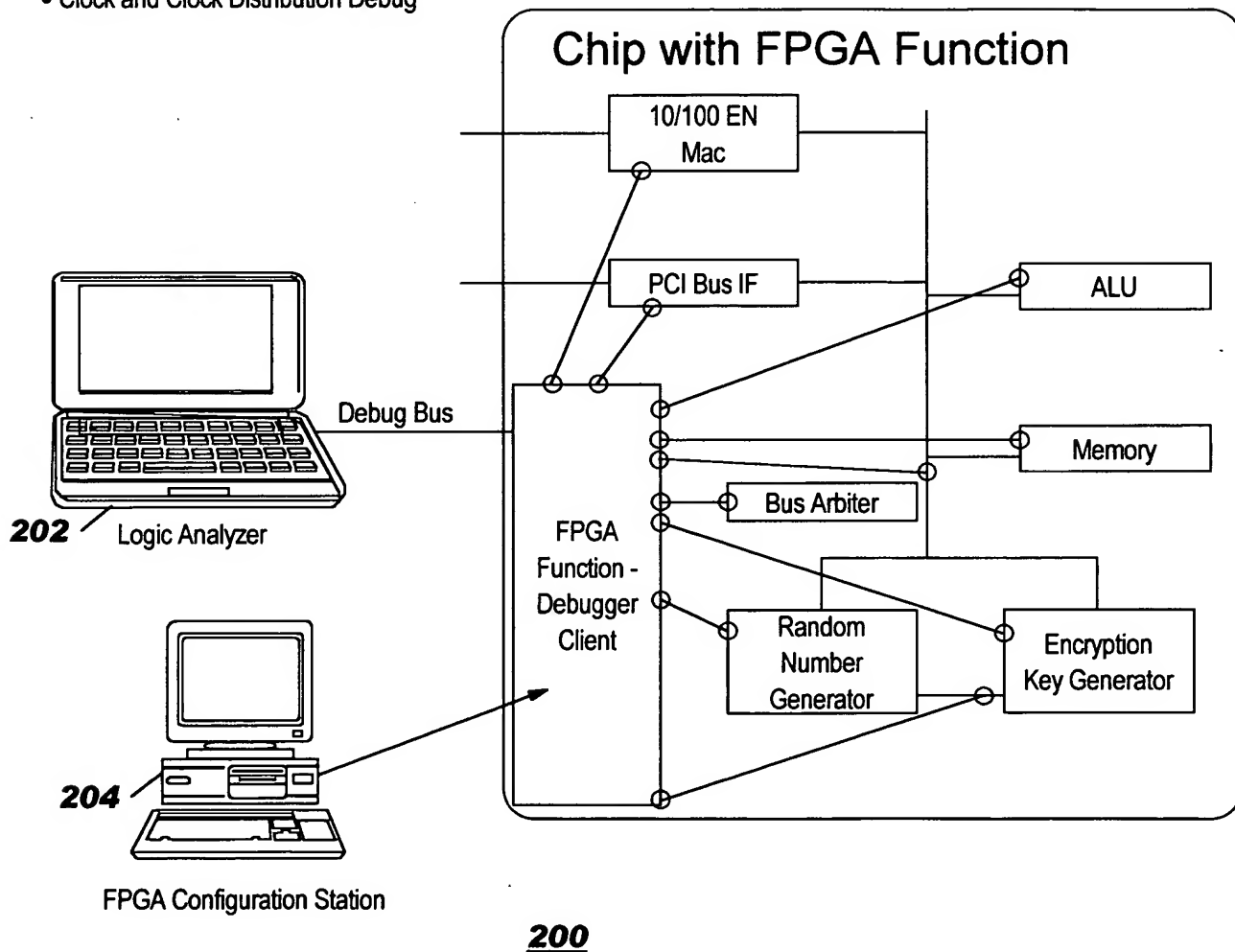


FIG. 5

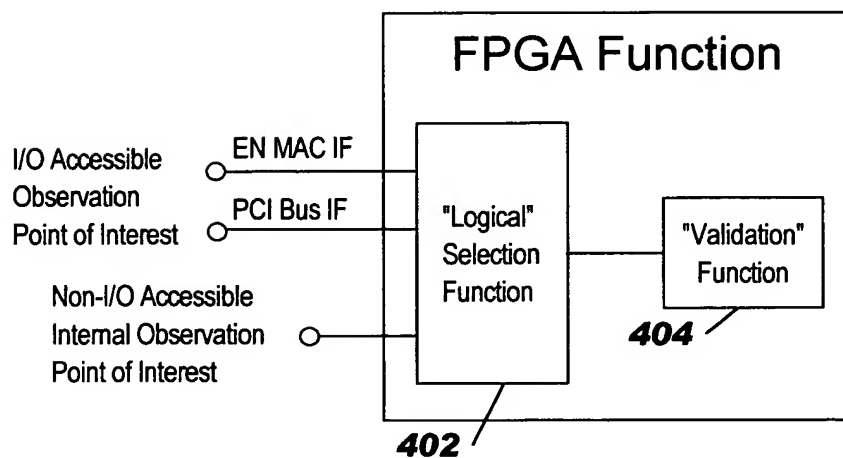


FIG. 6

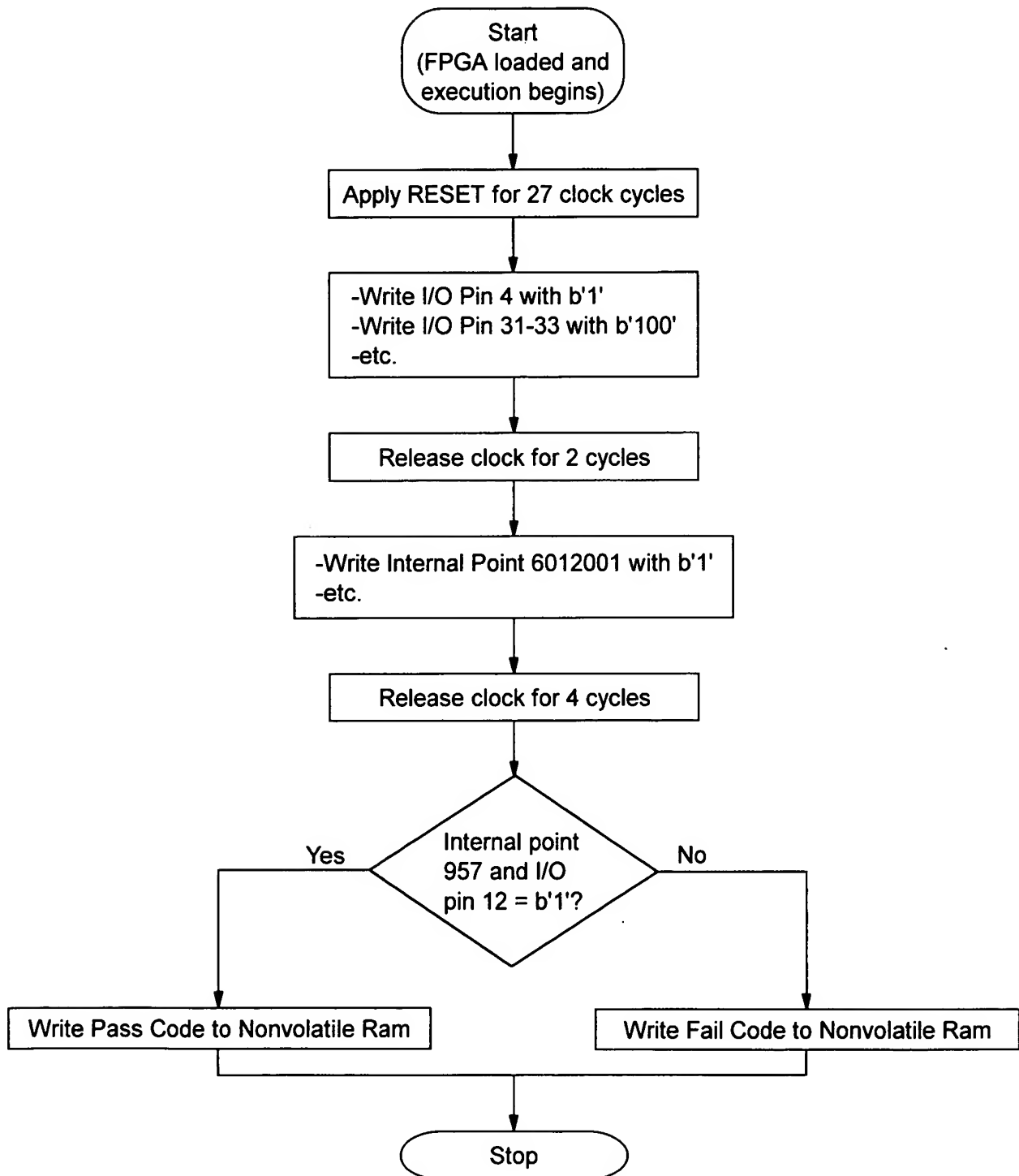


FIG. 7

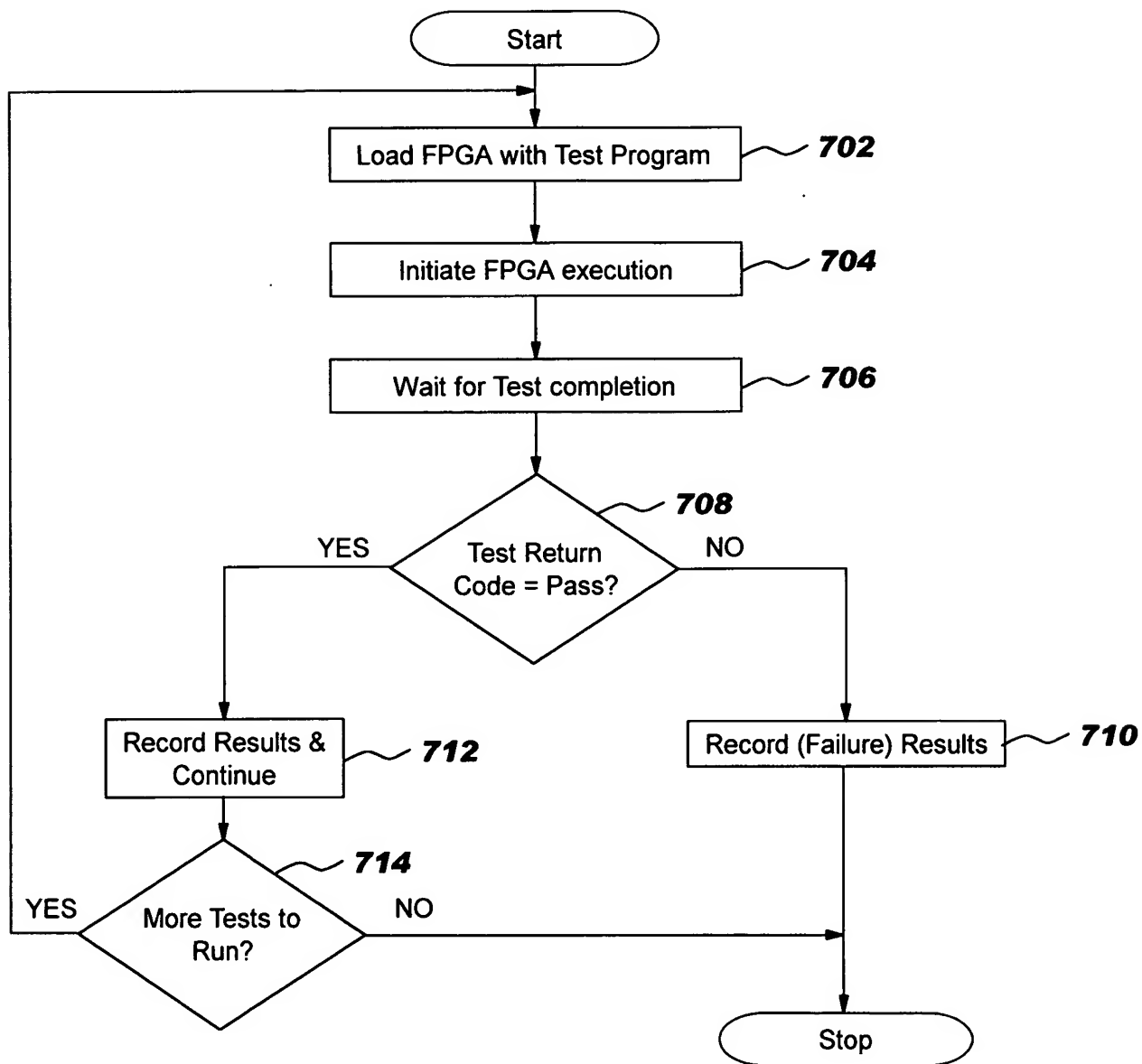


FIG. 8

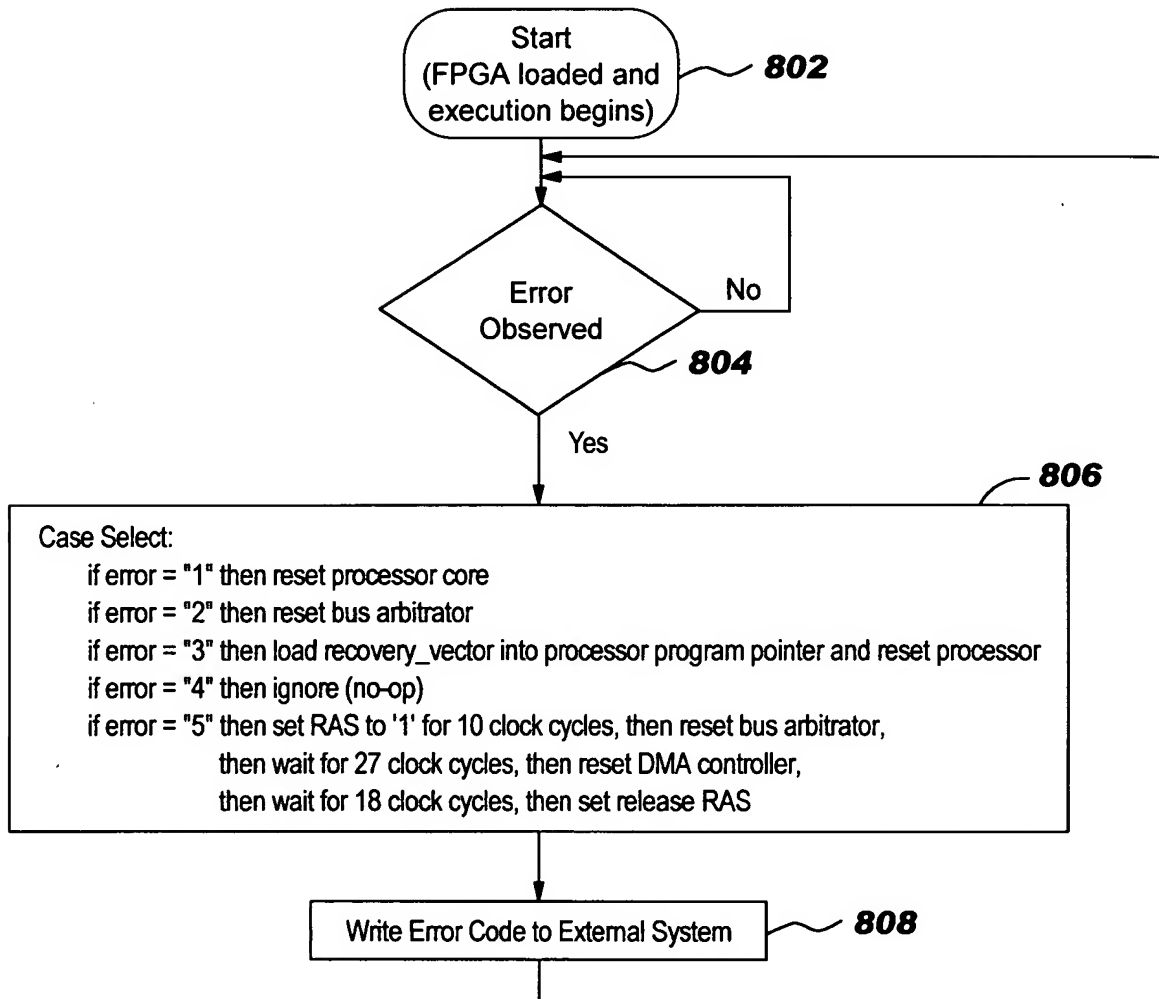




FIG. 9

